

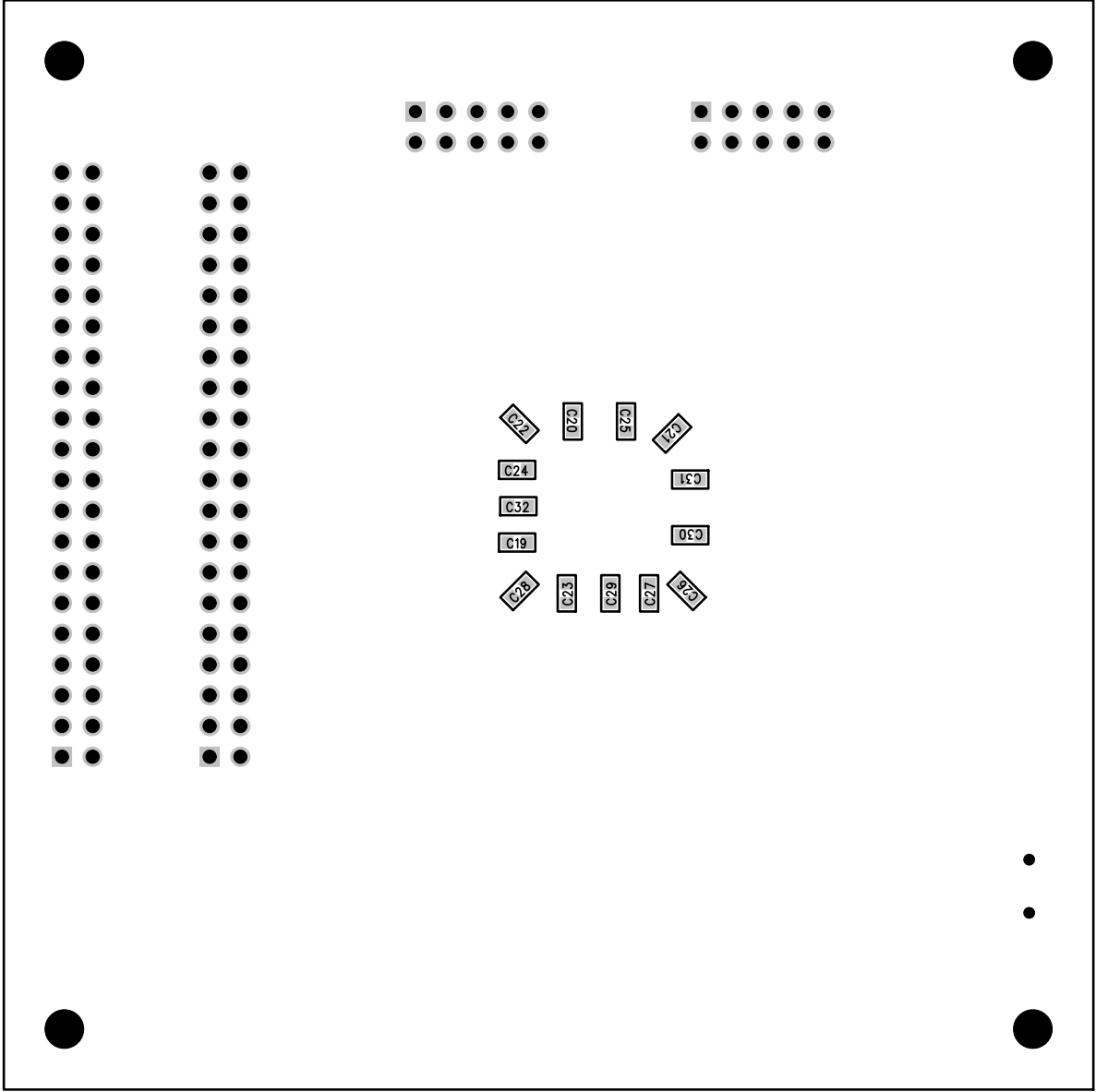
JTA Design

USB-FPGA HSIO

Layer: Assembly Drawing Top

Design date: 2013-01-16

Version: 01



Version: 01
Design date: 2013-01-16
Layer: Assembly Drawing Bottom
USB-FPGA H2IO
JTA Design