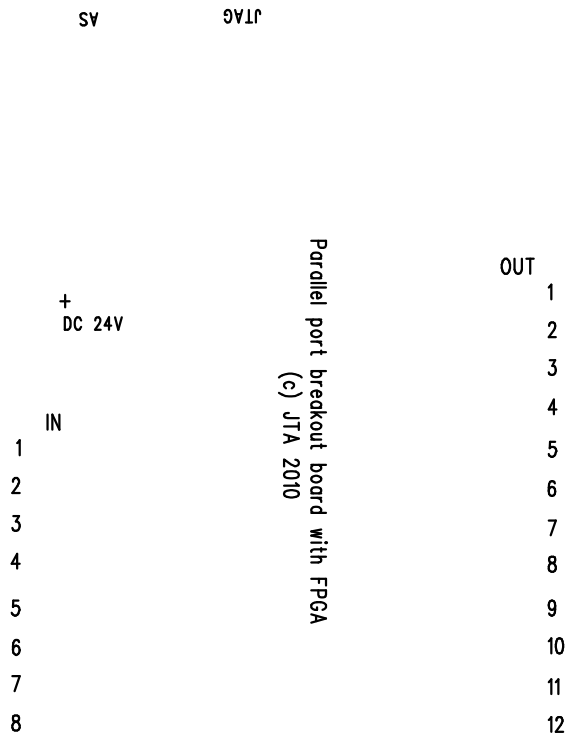
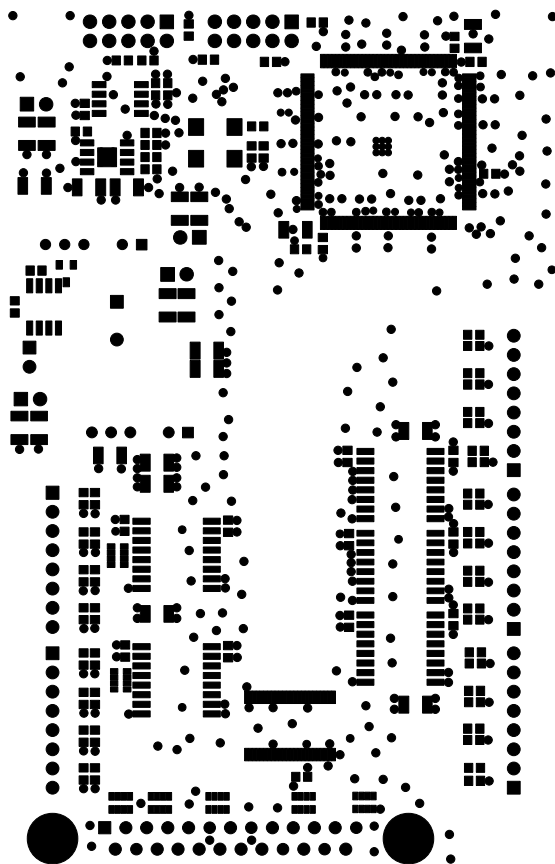


SIZE	QTY	SYM	PLATED	TOL
0.3	14	+ ^C	YES	+/-0.0
0.4	49	+ ^B	YES	+/-0.0
0.5	349	+	YES	+/-0.0
0.8	10	⊠	YES	+/-0.0
1	55	◇	YES	+/-0.0
1.1	42	⊠	YES	+/-0.0
3.2	2	⊠	YES	+/-0.0
1.1	2	+ ^A	NO	+/-0.0
3.2	3	⊠	NO	+/-0.0

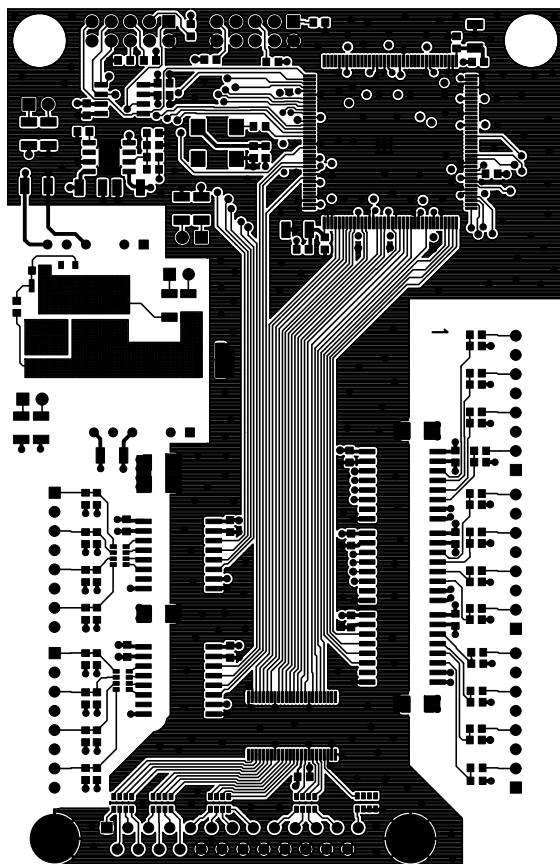
JTA Design
Breakout board
Layer:Drill Drawing
Design date: 2010-01-30
Version: 01



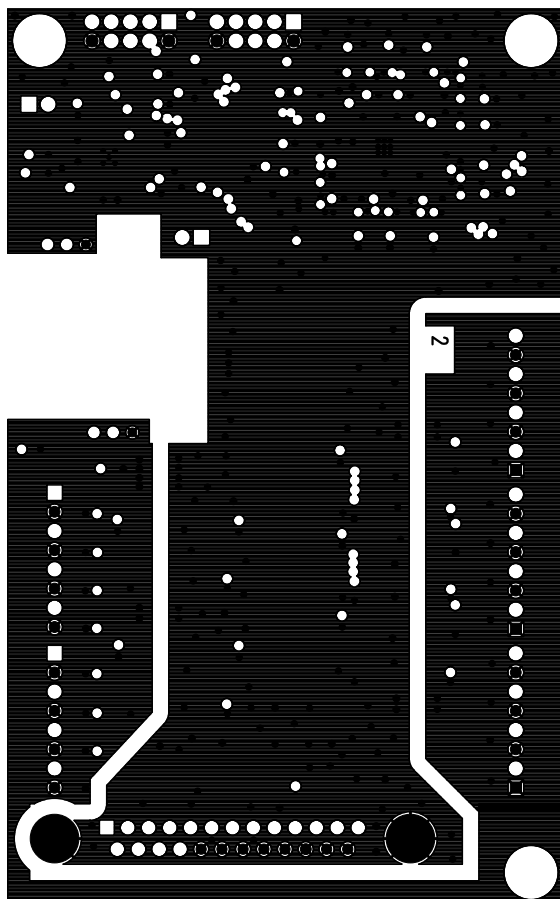
JTA Design
Breakout board
Layer:Top Silkscreen
Design date: 2010-01-30
Version: 01



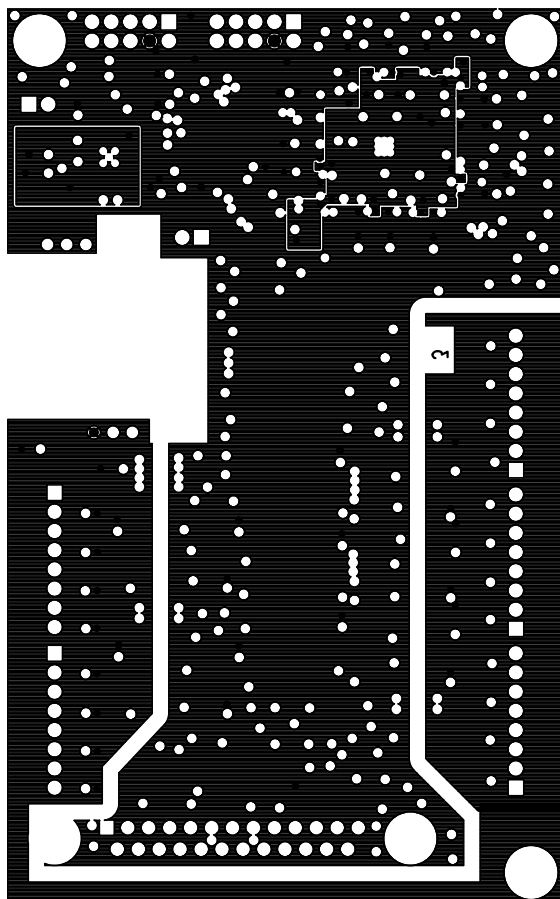
JTA Design
Breakout board
Layer:Solder Mask Top
Design date: 2010-01-30
Version: 01



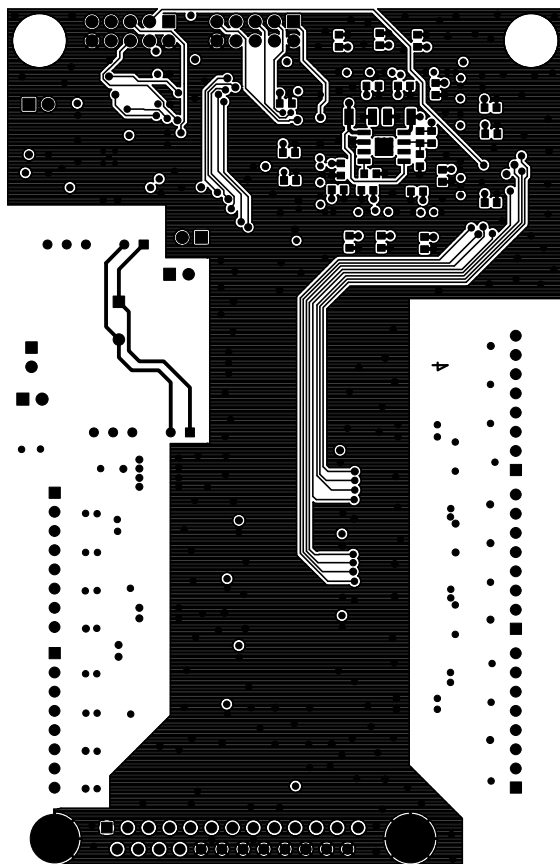
JTA Design
Breakout board
Layer:Top Layer 1/4
Design date: 2010-01-30
Version: 01



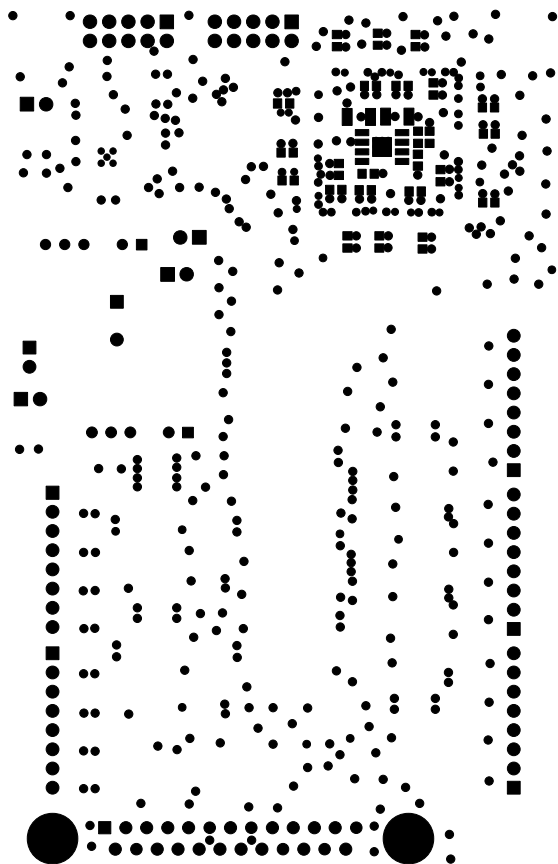
JTA Design
Breakout board
Layer:Layer 2/4
Design date: 2010-01-30
Version: 01



JTA Design
Breakout board
Layer:Layer 3/4
Design date: 2010-01-30
Version: 01



JTA Design
Breakout board
Layer:Bottom Layer 4/4
Design date: 2010-01-30
Version: 01



JTA Design
Breakout board
Layer:Solder Mask Bottom
Design date: 2010-01-30
Version: 01

