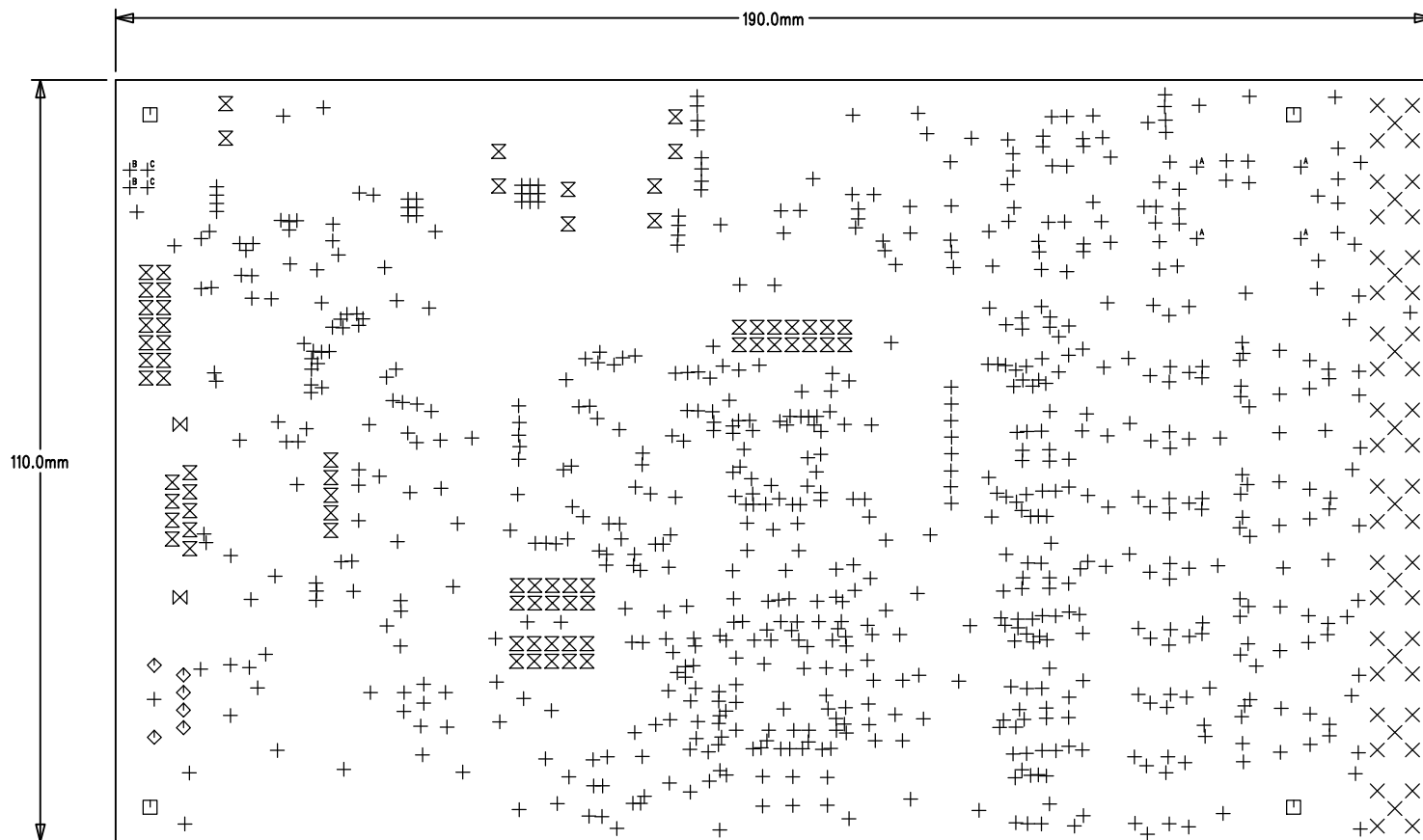
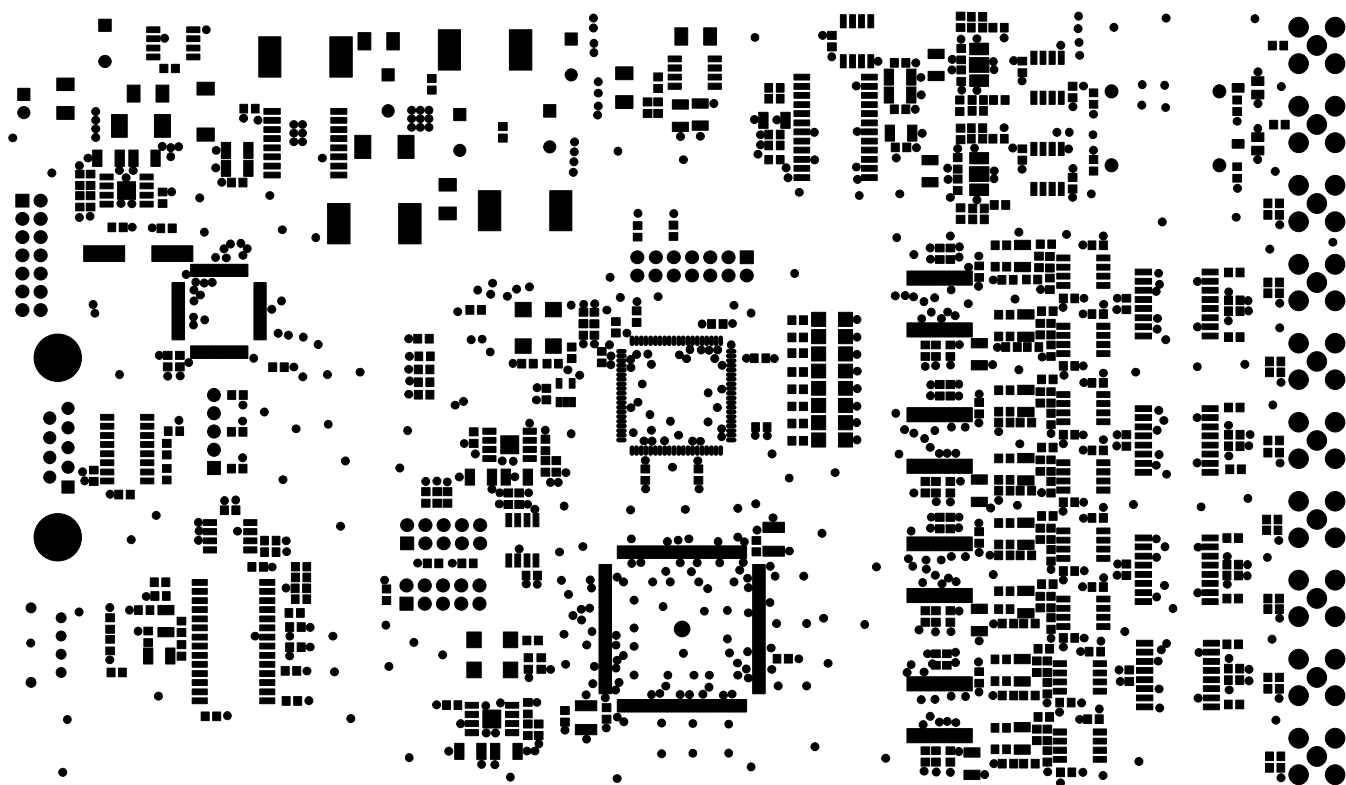


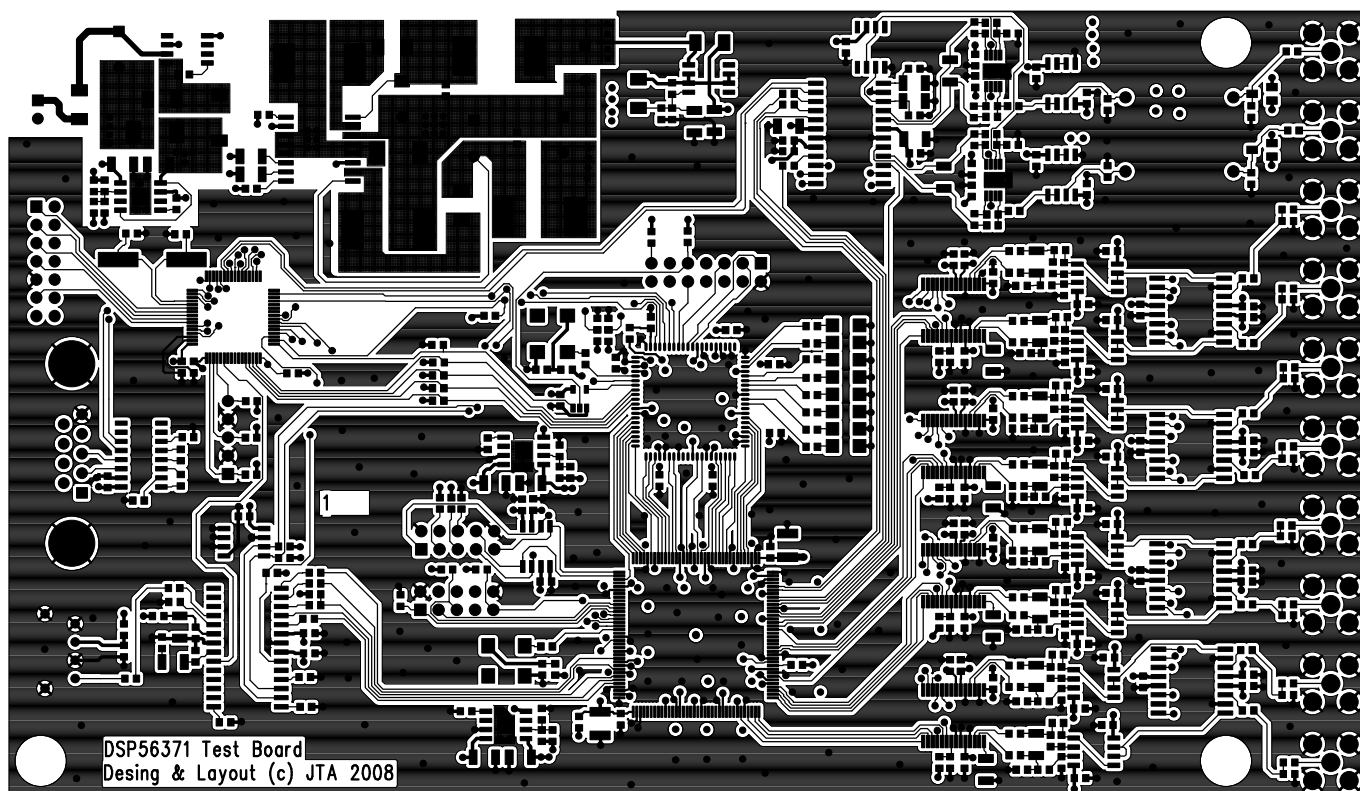
SIZE	QTY	SYM	PLATED	TOL
0.5	724	+	YES	+/-0.0
0.8	6	◇	YES	+/-0.0
1	72	⊗	YES	+/-0.0
1.1	2	+ ^C	YES	+/-0.0
1.2	4	+ ^A	YES	+/-0.0
1.7	50	×	YES	+/-0.0
3.2	2	⊗	YES	+/-0.0
1.1	2	+ ^B	NO	+/-0.0
3.2	4	□	NO	



JTA Design	
DSP56371 Test board	
Layer	Drilldrawing
Design date: 2008-04-16	
Version: 01	



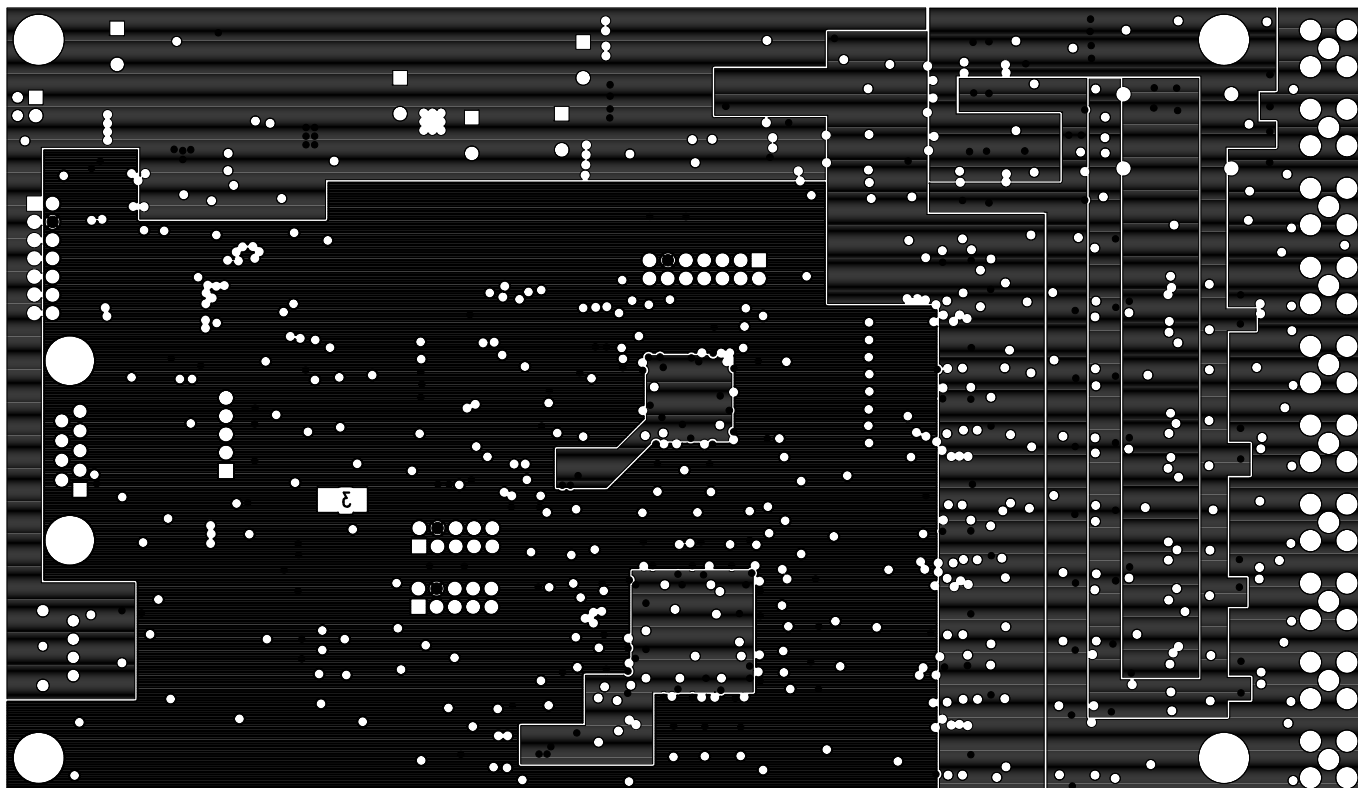
JTA Design
DSP56371 Test board
Layer Solder Mask Top
Design date: 2008-04-16
Version: 01



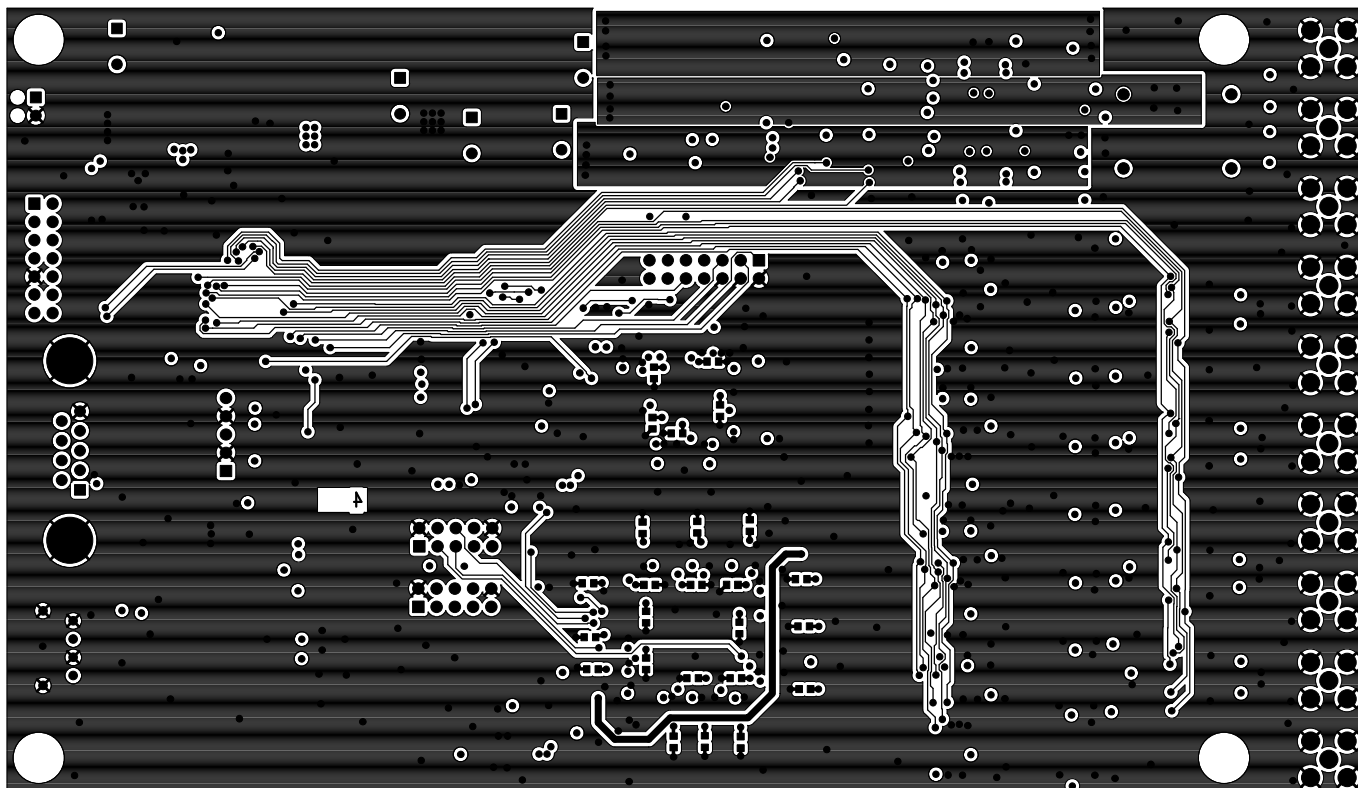
JTA Design	
DSP56371 Test board	
Layer	Top Layer
Design date: 2008-04-16	
Version: 01	



JTA Design
DSP56371 Test board
Layer Layer 2/4
Design date: 2008-04-16
Version: 01



JTA Design
DSP56371 Test board
Layer Layer 3/4
Design date: 2008-04-16
Version: 01



JTA Design
DSP56371 Test board
Layer Bottom Layer
Design date: 2008-04-16
Version: 01



JTA Design	
DSP56371 Test board	
Layer	Solder Mask Bottom
Design date: 2008-04-16	
Version: 01	



